

# The latest activities related to the passive components in JAXA

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### Japan Aerospace Exploration Agency (JAXA)

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- Introduction of JAXA qualified passive components
- Comparison of JAXA/ESCC qualification test specification
- Recent news of passive components
  - Reliability test of printed circuit board for space-use
- Summary

## **Introduction of JAXA qualified** passive components



JAXA has the components qualification system for components applied to the space application.

Main purpose of this system

- Components which are applied for many satellites are treated as standard components.



Quality and reliability of standard components are totally confirmed by this system in advance. Qualification test (QT), Screening test, Stability of ML\*

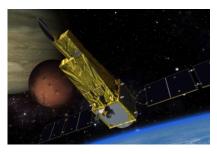
- Projects which plan to use such a standard components do not need any test before using.

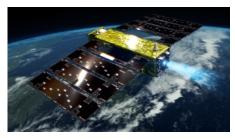
\* Manufacture line



ESA and NASA have similar qualification system.









# Introduction of JAXA qualified passive components



There is a total of 147 models of JAXA qualified components, of which 100 models are passive components.

\* PCBs and materials such as thermal control films are also included in JAXA qualified components.

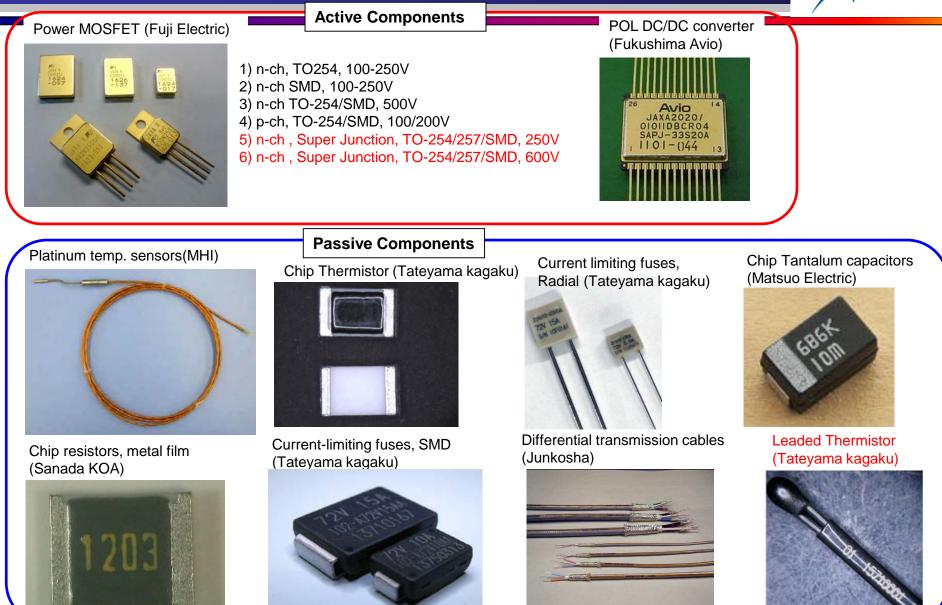
Comp. family	Description	Detail spec.	Manufacturer
Capacitors	Mica	4	Soshin Electric
	MLCC	3	Murata
	Chip, Solid, Electrolytic, Tantalum EPPL	1	Matsuo Electric
Resistors	Chip, Thick Film	1	Tateyama Kagaku
		2	Hokuriku Electric
	Wire-Wound (Power Type)	2	Seiden Techno
		1	Sanada KOA
	Film	3	Sanada KOA
	Networks, Film	1	Sanada KOA
	Chip, Thin Film EPPL	1	Sanada KOA
Thermistors	Chip, Negative Temperature Coefficient EPI	PL 1	Tateyama Kagaku
	Lead, Negative Temperature Coefficient	1	Tateyama Kagaku
Fuses	Subminiature, Current-Limiting EPPL	2	Tateyama Kagaku
Temp. Sensors	Platinum EPPL	3	MHI*
Osc. Crystals	Quartz Crystal Units	4	Nihon Dempa Kogyo
Transformers and	Power	2	Tamura
Inductors	Others	6	Tamura
Wires and Cables	Fluoroplastic, Polyimide Insulated Wires	4	Hitachi Metals
	Differential Transmission Cables EPPL	2	<u>Junkosha</u>
Connectors	Rectangular, Miniature	2	JAE**
		1	Nihon Maruko
	Rectangular, Miniature, High Density	2	JAE**
		1	Nihon Maruko
	Rectangular, Microminiature	1	Nihon Maruko
	Rectangular Miniature Mixed	1	Nihon Maruko
	Coaxial, RF	3	Waka Manufacturing

Table 1. List of JAXA qualified passive components.

\* MHI=Mitsubishi Heavy Industries \*\* JAE=Japan Aviation Electronics Industry

# **JAXA qualified components listed in EPPL**





Red: Newly appointed after 2006



Like a example of JAXA qualified components listed in EPPL, JAXA and ESA conducted the activity related to the Cooperation Agreement on EEE Components for mutual usage of each components.



Mutual usage of Japanese components in Europe / European components in Japan has been promoted by JAXA and ESA:

- To avoid duplicated development of similar components in Europe / Japan
- to ensure the availability of second source

To remove the barrier of mutual usage, JAXA and ESA held joint work to compare JAXA specifications and ESCC specifications for component qualification.

Items to be compared

- **1. Overall difference in both qualification systems**
- 2. Detail comparison of generic specifications



Document tree has been compared between JAXA and ESA qualification system.

Same document tree from Level 2 to Level 4

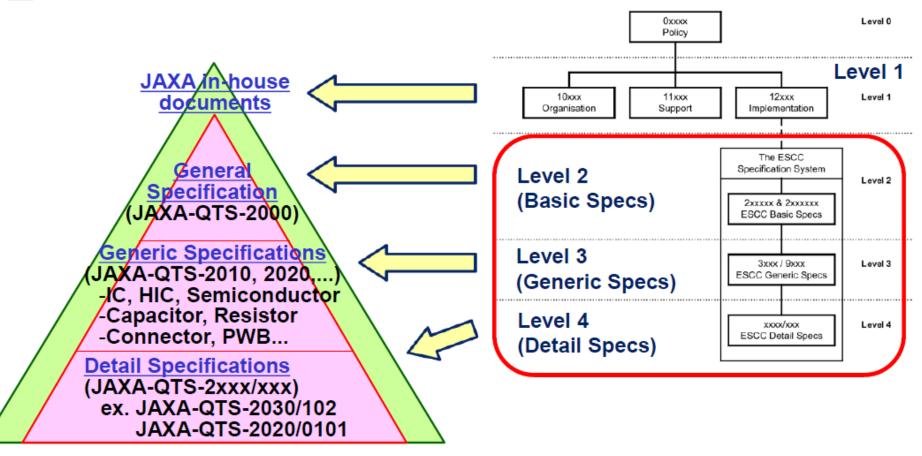


Fig. 1. Document tree of JAXA qualification system and ESCC qualification system



Summary of the overall comparison each qualification system are listed below.

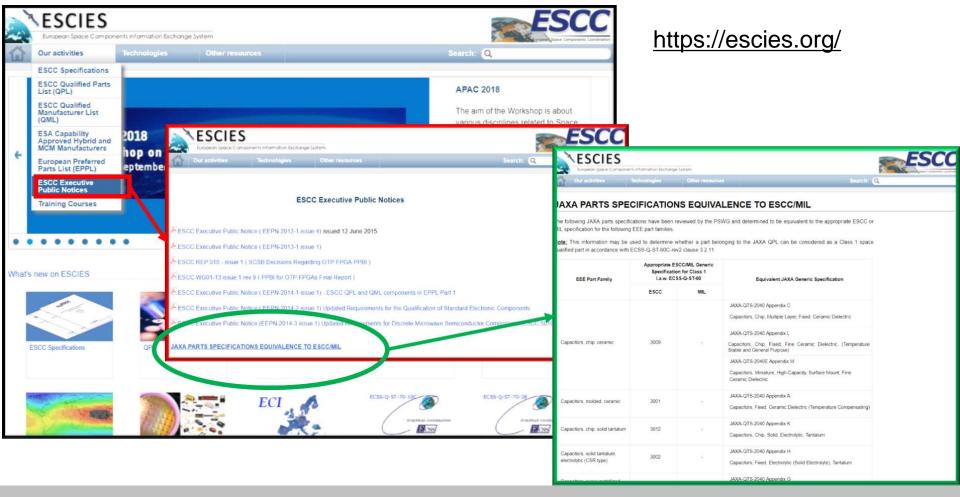
There is <u>no major difference</u> when compared JAXA system with ESCC system.

System	ESCC	JAXA
Type of qualification	<ul> <li>Component Qualification</li> <li>Technology Flow (TF)</li> <li>Capability Approval</li> <li>(- Process Capability Approval)</li> </ul>	<ul> <li>QML (qualification of manuf. lines)</li> <li>QPL (qualification of parts)</li> </ul>
Duration	2 years	3 years (QML)
Quality management	Process Identification Document (PID) + QMS	Quality Assurance Program Plan (QAPP)
Manufacturing line	Commercial lines may be used (processes, materials and technology shall be frozen by PID)	Commercial lines may be used (processes, materials and technology shall be frozen by QAPP)
Change control of QA program	<ul> <li>Review / approval required by ESCC Executive</li> <li>Decision can be made by TRB with limitation (TF)</li> </ul>	Decision can be made by TRB with limitation (QML)
Required tests for MoQ / procurement	<ul> <li>In-process control</li> <li>Screening test</li> <li>Periodic tests (every 24 or 12 months) <ul> <li>Environmental / mechanical subgroup</li> <li>Endurance subgroup</li> <li>Electrical subgroup</li> <li>Assembly / capability subgroup</li> </ul> </li> </ul>	<ul> <li>In-process inspection</li> <li>Screening test (active parts)</li> <li>Quality Conformance Inspection         (test interval differ from test group)         <u>passive parts : Group A-C</u>         basic characteristics ,life test etc.         <u>active parts : Group A-E</u>         electrical tests, die related tests, package related tests, radiation test etc.     </li> </ul>
Available Docs. after certification	- Detail specification	<ul> <li>Detail specification</li> <li>Application Data Sheet (ADS)</li> </ul>

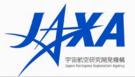


Generic specification documents of all JAXA qualified components were compared with ESCC's specification. (<u>26 items</u>' equivalence has been confirmed)

Comparison results are indicated in ESCIES.org (ESCC Public domain website).



## **Recent news of passive components**



Conventionally, the PCBs for space-use were provided by almost one qualified manufacturer (Manufacturer A) for Japanese space project.

The manufacturer A could not continue to provide their products due to shutting down of the factory since the development for magnetic levitation training planned through their factory premises.



The manufacturer of PCBs for space-use recently transferred the qualified processes to an alternative JAXA qualified manufacturer (Manufacturer B).

# Reliability test of printed circuit board for space-use



Prior to the actual replacement, we evaluated the characteristics and reliability of the printed circuit test boards manufactured by each manufacturer under the same conditions.

Evaluation items

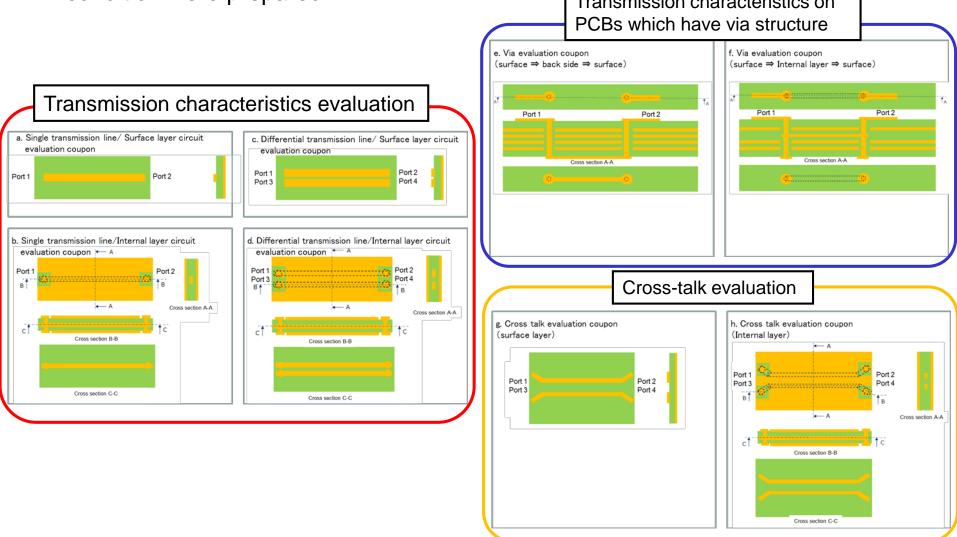
### 1. Electrical characteristics

- Manufactured evaluation test coupons by each manufacturers.
- S parameter measurements
- Simulation based on the measured S parameters about characteristic impedance, dielectric constant and transmission loss
- Cross-talk characteristics
- 1. Long-term reliability
  - Manufactured evaluation test coupons by each manufacturers.
  - Interconnect stress test (IST) & DPA

### **Electrical characteristics evaluation**

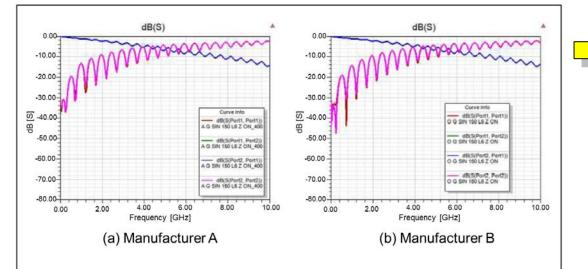


8 types of test coupons manufactured by each manufacturer under the same condition were prepared.



## **Electrical characteristics evaluation**

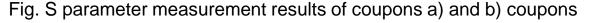




Although a slight difference existed on reflection coefficient below the frequency of 1 GHz between two manufacturers, the difference was less than -20 dB.



It a negligible difference for electrical characteristics.



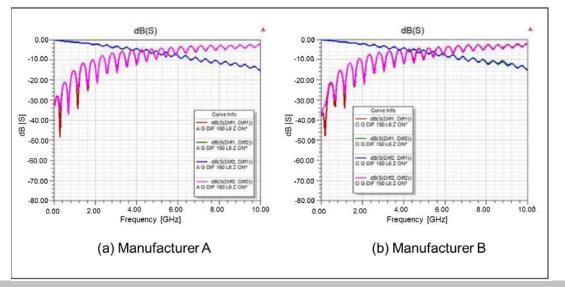


Fig. S parameter measurement results of coupons c) and d) coupons

## **Electrical characteristics evaluation**



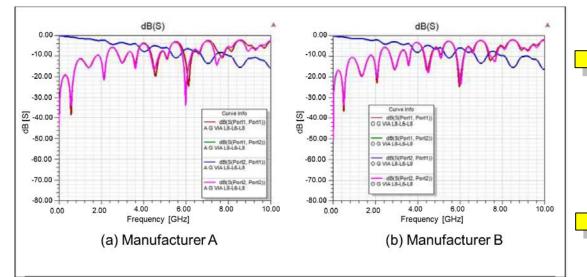
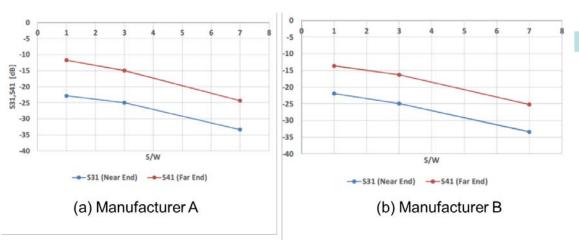


Fig. S parameter measurement results of coupons e) and f) coupons



There is a slight difference on the peak around 6 GHz, but the difference has no effect on the circuit electrical characteristics or the function.

On the cross-talk evaluation, coupons made by manufacturer B indicated lower cross talk level than Manufacturer A's coupons.

The risk of switching the printed board manufacturers is low related to the crosstalk characteristics.

We concluded that there is no risks for replacing the manufacturer related to

Fig. Cross-talk characteristics measurement results of coupons g) and h) electrical characteristics.

## Interconnect stress test (IST) & DPA



- In IST, the samples are heated directly by a current flowing through a self-heating circuit in the samples, and cooled by forced air cooling.
- By repeating this cycle, the temperature cycling is carried out and stress is applied to the PCBs in a short period of time.
  - Long term reliability of PCBs can be evaluated

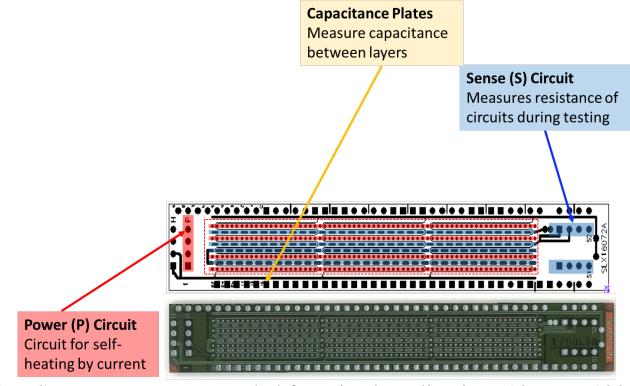


Fig. IST coupon recommended for HiRel application (18mm x 100mm)

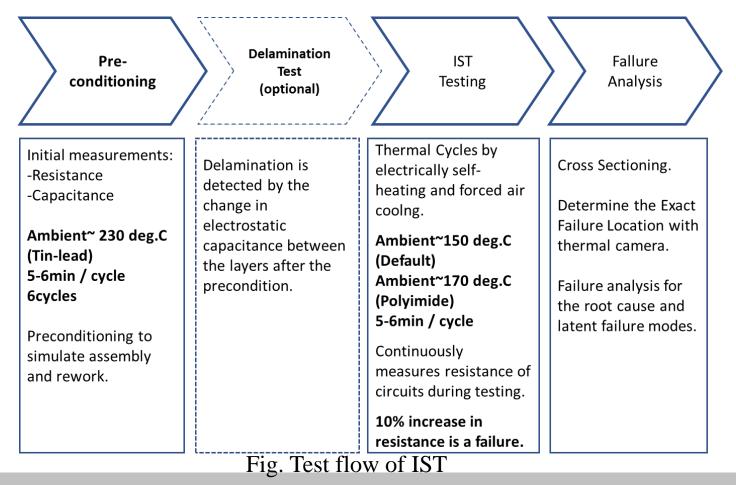
## Interconnect stress test (IST) & DPA



IST is performed on the test coupon after pre-conditioning.

The resistance of the circuits is measured continuously during the IST.

10% increase in resistance is identified as a failure.





Specification of test coupons and IST results are summarized below tables.

	<b>FR-4</b>	Polyimide
Board Material	Epoxy(FR-4)	Polyimide(GI)
Layer count	6	12
Board Thickness	2.4 mm	3.2 mm
Drilled Hole Diameter	0.7 mm	0.35 mm
Number of samples	6 coupons	6 coupons
	(6 circuits)	(12 circuits)

Table. Specification of test coupons

#### Table. Summary of the IST results

Coupon	FR-4		Polyimide	
IST Cycles	Manufacturer A	Manufacturer B	Manufacturer A	Manufacturer B
Min	304	285	>2000(6.0%)	649
Max	628	400	>2000(3.6%)	859
Mean	455	352	>2000(4.9%)	786
StDev	120	39	-	72
Range	324	115	-	213

Both types of manufacturer B coupons increased resistance by 10% with fewer IST test cycles. No difference in drill condition or copper plating quality etc. were observed.

## Interconnect stress test (IST) & DPA



Plating cracks

According to the destructive physical analysis (DPA) results, cracks on the through hole wall were observed and the shape and the position of cracks were similar.

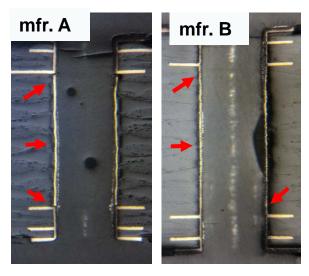


Fig. Cross-section : After IST FR-4 PCBs

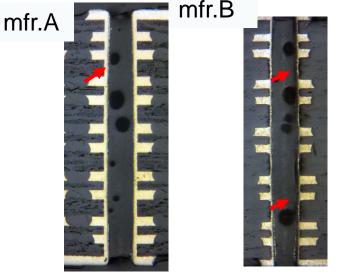


Fig. Cross-section : After IST Polyimide PCBs

#### Table. Copper plating thickness of each sample

	FR-4		Polyi	mide
Manufacturer	А	В	А	В
Copper Plating Thickness	39µm	28µm	41µm	31µm

The difference in the number of IST cycles for 10% increase of resistance is simply due to the difference in the thickness of copper plating.

## JAXA qualification test for Mfr. B



PCBs manufactured by Manufacturer B were also provided to the JAXA qualification test which include thermal cycle test, electric characteristic evaluation test, etc..



Manufacturer B's PCBs passed all qualification tests.

It was verified that Manufacturer B's PCBs satisfied the JAXA qualification test requirements.

And these were qualified to JAXA qualification components.

## Summary



- An overview of JAXA qualified passive components and their qualification requirement was introduced.
- Currently there are 100 JAXA qualified passive components and 11 of them are listed in EPPL.
- The qualification system in JAXA is quite similar to that in ESCC and its general requirements are outlined in comparison with those in ESCC system.
- As the result of comparison, the qualification test requirements of JAXA qualification system are verified to be equivalent to that of ESCC system.
- As a recent news, for the replacement of PCB manufacturer, the comparison evaluation results of PCBs which made by two manufacturers were reported.
- Electrical characteristics evaluations and IST for long-term reliability were performed and it was confirmed that the quality and performance of two products from these manufacturers are practically equal.
- We concluded that there is no risks for replacing the PCB manufacturer.

# Thank you for your attention!!



# **APPENDIX**

# 1. Objectives

Thermal cycle tests have been applied as one of reliability evaluation tests for Printed Circuit Boards (PCBs) for space-use, but these tests takes a long time. (Thermal Shock Test 1000cycles : more than 1.5 month)

Recently, **Interconnect Stress Tests (IST)** have been standardized by IPC and adopted globally as a method that can evaluate the connection reliability of PCBs in a short period of time.

Therefore, we evaluated the effectiveness of IST as a test method capable of thermal cycling test in a short time.

In this evaluation, comparison between thermal shock test and IST, and comparison of two different PCBs by IST test were conducted.

# 2. Overview of IST2.1 The principle of IST

In thermal cycle test of the PCBs, the difference of thermal expansion coefficients (CTE) between Copper and Substrate materials becomes a source of stress causing cracks in the through holes, resulting in failures such as resistance increase, open circuit, separation of plating from base metal etc.

In the air-to-air thermal shock test, the samples are heated and cooled by changing the temperature in the test chamber, and stress is given by the difference in CTE between Copper and Substrate materials .

In IST, the samples are heated by directly flowing a current through a self-heating circuit in the samples, and cooled by forced air cooling. By repeating this cycle, the temperature cycling is carried out and stress is applied to the PCBs in a short period of time.

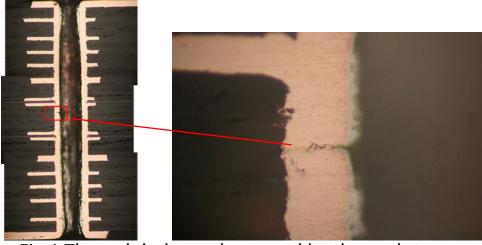


Fig.1 Through hole cracks caused by thermal stress

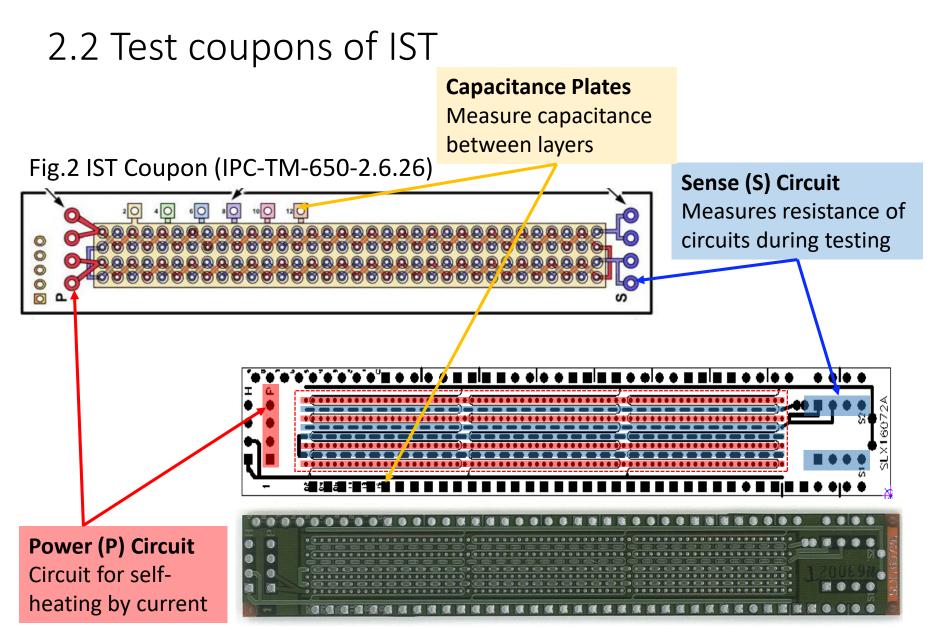
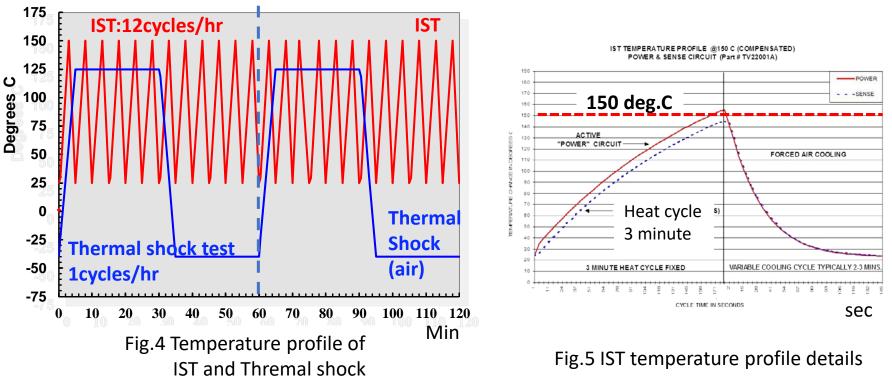


Fig.3 IST coupon recommended for HiRel application (18mm x 100mm)

# 2.3 Test conditions of IST and thermal shock

A comparison of the temperature profile of **IST and** the **Thermal shock test (air-to-air)**.



Since the IST test is self-heating and forced air cooling, a short-time temperature change can be realized.

The number of test cycles of thermal shock test including soak time is 1 cycle/hr, while IST can be 12 cycles/hr.

In addition, IST is considered to be a severe stress test because the ramp-rate of temperature is larger than the thermal shock test.

# 2.4 Test flow of IST

Pre- conditioning	Delamination Test (optional)	IST Testing	Fallure Analysis
Initial measurements: -Resistance -Capacitance Ambient~ 230 deg.C (Tin-lead) 5-6min / cycle 6cycles Preconditioning to simulate assembly and rework.	Delamination is detected by the change in electrostatic capacitance between the layers after the precondition.	Thermal Cycles by electrically self- heating and forced air coolng. Ambient~150 deg.C (Default) Ambient~170 deg.C (Polyimide) 5-6min / cycle Continuously measures resistance of circuits during testing. 10% increase in resistance is a failure.	Cross Sectioning. Determine the Exact Failure Location with thermal camera. Failure analysis for the root cause and latent failure modes.

Comparative test of the IST and Thermal shock test
 Comparative test

We conducted both IST and thermal shock test on PCBs which were manufactured in the same process, and compared the evaluation results.

The test conditions are shown below.

#### Table.1 Test condition of Theramak Shock and IST

	Thermal Shock Test	IST
Preconditioning	Reflow 3 cycles (max temp. 240 deg.C)	230 deg.C 6 cycles
Cycle Test	-30 deg.C / 30 min 🔶 125 deg.C / 30 min	Ambient~170 deg.C (Polyimide) Ambient~150 deg.C (Low-Dk)
Number of sample	3	6
Test termination	10% increase in resistance or at completion of 3000cycles	10% increase in resistance

## 3.2 Test coupons (IST / Thermal Shock Test)

#### Table.2 Specification of test coupons

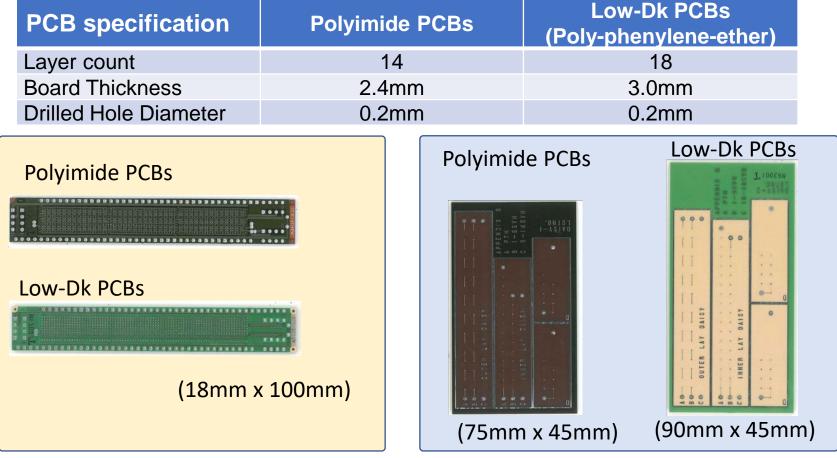
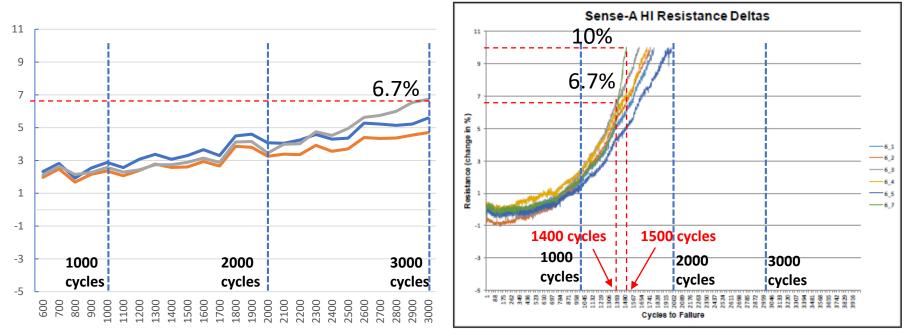


Fig.6 IST Coupons

**Fig.7 Thermal Shock Test Coupons** 

These test coupons were manufactured by the same manufacturer and the same production conditions such as plating thickness. <sup>28</sup>

### 3.3 Results (1/4) Resistance Change of Polyimide PCBs



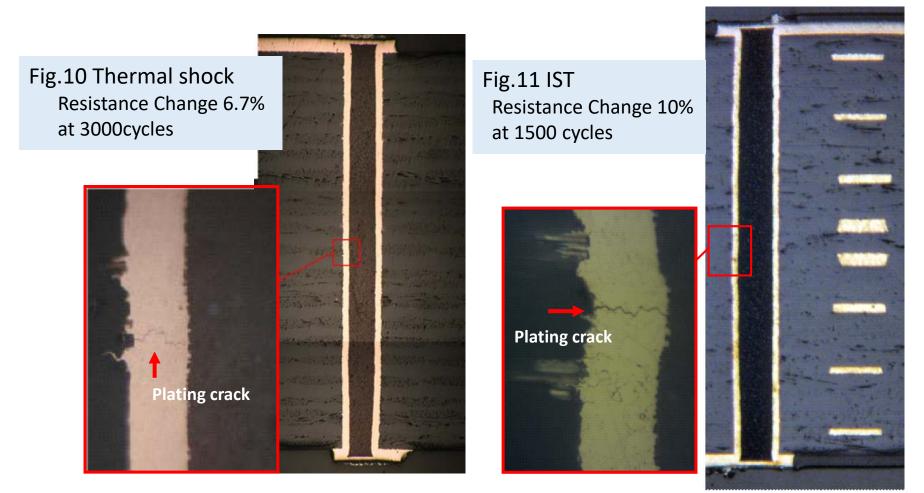
### Fig.8 Thermal shock test Resistance Change

Fig.9 IST Resistance Change

The number of cycles at which the resistance change reached 6.7% was compared:Thermal shock test:3,000cycles (about 3,000 hour)IST:1,400cycles (about 116 hour)

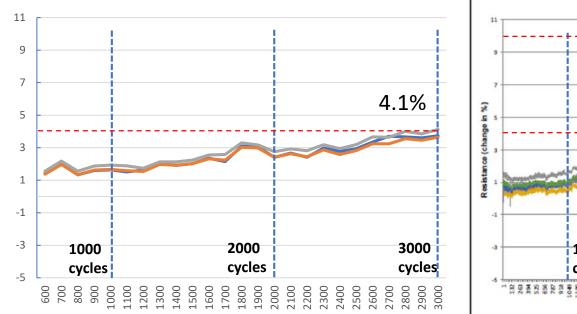
As you can see, the IST was completed in a much shorter period of time. The same resistance increase occurred in IST in 1/20 the time shorter and 1/2 cycle less than thermal shock test.

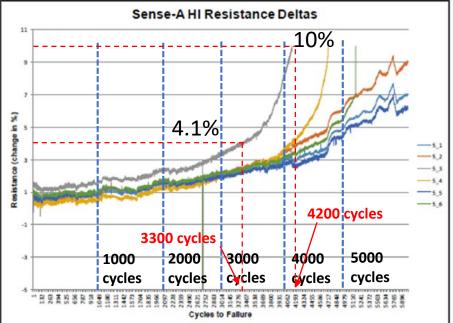
## 3.3 Results (2/4) Cross section of Polyimide PCBs



The cross section of the through-hole where resistance increased were observed. As a result, in both thermal shock test and IST, the through hole plating cracks (barrel cracks) were occurred at similar positions and these failure modes were the same. 30

## 3.3 Results (3/4) Resistance Change of Low-Dk PCBs





### Fig.12 Thermal shock test Resistance Change

Fig.13 IST Resistance Change

The number of cycles at which the resistance change reached 4.1% is compared:

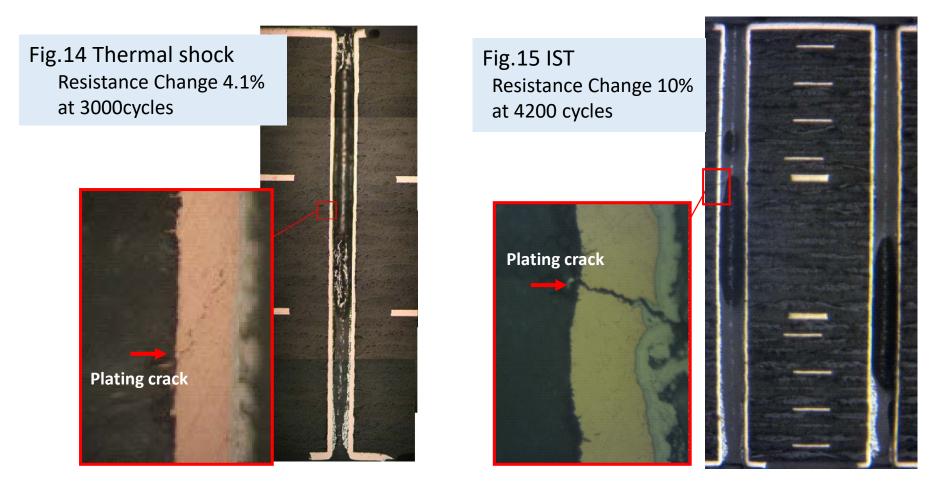
Thermal shock test: 3,000 cycles (about 3,000 hour)

IST: 3,300 cycles (about 275 hour)

The IST was completed in a much shorter period of time.

Unlike polyimide PCBs, IST did not accelerate cycle number, but the same resistance increase occurred at about 1/10 or less time.

## 3.3 Results (4/4) Cross section of Low-Dk PCBs



Through-hole cracks (barrel cracks) of the same mode occurred at similar positions. Although cracks of thermal shock test are small, if the test cycles is continued to reach the same resistance change as the IST, the cracks are most likely to grow to a similar size.

# 3.4 Discussion(1/2)

As a result of the comparative test, the failure modes of both IST and Thermal shock were through-hole plating cracks.

As the position and shape of the plating cracks in through-hole were similar, we believe that IST and thermal shock test are equivalent reliability evaluations.

As a result, we consider it possible to conduct the thermal cycle test in a short time by using the IST.

# 3.4 Discussion(2/2)

As a result of the evaluation, for the Polyimide PCBs, the IST showed an accelerated cycle number of the resistance increase compared to the thermal shock test. But for Low-Dk PCBs, the acceleration was different from polyimide PCBs.

One of the reason why the acceleration was different between the differences in the materials in the PCBs of under the same manufacturing condition is considered to be the difference of the z(thickness direction)-CTE of the materials.

In IST, the ramp-rate of temperature is large, stress due to the CTE difference is large and it is applied in a short time, so cracks are generated in fewer cycles.

	Thermal shock	IST	Tharmal shock/	z-CTE(p	pm)	ΔCTE
PCBs	cycles	cycles	IST	material	Copper	(ppm)
Polyimide	<b>3000</b> (Resistance Increase 6.7%)	1500 (Resistance Increase 6.7%)	≒2.0	70	17	53
Low-Dk	3000 (Resistance Increase 4.1%)	3300 (Resistance Increase 4.1% )	≒1.0	45		28

Table.3 IST cycles and CTE

It is difficult to directly compare the tests with different materials of PCBs in IST because they have different accelerations.

However, in the comparison of the tests with the same materials, the same failure mode occurs in a short period of time, so IST is considered to be effective as a thermal cycle test.

# 4. Comparison of PCBs of two different manufacturers4.1 Purpose of comparison

In the IST test, it is considered that thermal cycling tests can be conducted in a short period of time between the same material PCBs.

So samples with the same design and materials manufactured by two manufactures (manufacture A and B) with different manufacturing processes were tested using IST.

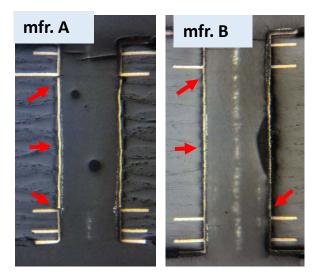
	FR-4 PCBs	Polyimidel PCBs	
Board Material	Epoxy(FR-4)	Polyimide(GI)	
Layer count	6	12	
Board Thickness	2.4 mm	3.2 mm	
Drilled HoleDiameter	0.7mm	0.35mm	
Number of complex	6 coupons	6 coupons	
Number of samples	(6 circuits)	(12 circuits)	
Table.5 Test condition c	of IST		
	IST co	ondition	
Temperatures		deg.C(#1.FR-4) g.C(#2.Polyimide)	
Test Time(min/cycles)	5min		
Test termination	Max. 2000 cycles or Resistance Change10%		

#### Table.4 Specification of test coupons

# 4.2 Results Comparative test by IST

#### Table6. IST cycle

Coupon	FR-4 PCBs		Polyimide PCBs	
IST Cycles	mfr.A mfr.B		mfr.A	mfr.B
Min	304	239	>2000(6.0%)	509
Max	628	354	>2000(3.6%)	788
Mean	455	292	>2000(4.9%)	657
StDev	120	40	—	93
Range	324	115	—	279



Plating cracks

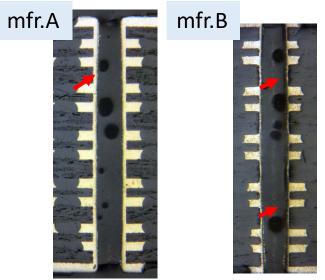


fig.16 Cross-section : After IST FR-4 PCBs

fig.17 Cross-section : After IST Polyimide PCBs

On both types of PCBs, mfr. B reached 10% resistance increase with fewer IST test cycles. No difference drill condition or copper plating quality etc. were observed. Both of the failure modes were barrel cracks of through holes.

# 4.3 Discussion(1/2)

The result of the comparison between the two manufactures showed that the increase in resistance of mfr. B requires less cycle numbers than mfr. A.

DPA showed there was no difference in the roughness of the through hole wall, the particle of the copper plating, and the state of the inner layer connection with the PCBs between these manufactures' samples.

However, there was a difference in through hole copper plating thickness; mfr. B's was thinner. It is considered that the difference in the number of cycles of the IST depends on the copper plating.

		FR-4	PCBs	Plyimide PCBs		
	Manufacture	А	В	А	В	
	Copper Plating Thickness	39µm	22µm	41µm	28µm	
N	1anufacturer A 39µm		Manu	Ifacturer B		

#### Table.7 Copper Plating Thickness of each sample

Fig.18 Cross section : Through hole FR-4

# 4.3 Discussion (2/2)

In order to confirm the influence of the copper plating thickness on the IST test cycles, the through hole plating was thickened on the substrate of company B and evaluated again. The manufacturing conditions other than the plating thickness were the same.

The numbers of IST cycles at resistance increase of 10% are shown in the table 8.

Coupon	FR-4 PCBs		Polyimi	dePCBs
Plating Thickness	22µm 🗖	28µm	28µm	→ 31µm
Min	239	285	509 🗖	646
Max	354	400	788	859
Mean	292	352	657	786
StDev	40	39	93	72
Range	115	115	279	213

#### Table.8 IST cycle for Each Plating Thickness

As the thickness of copper plating became thicker in any of the board materials, IST cycle number has increased.

From this result, it is concluded that the number of cycles at resistance increase of 10% changes as the copper plating thickness changes.

However, there is concern that if the IST test result is evaluated only by the number of cycles, it may lead to an incorrect conclusion.

Thus, It is important to implement DPA in order to determine whether failure factors <sub>38</sub> other than plating thickness exist.

# 5. Conclusion

As a result of evaluation on IST, the effectiveness found is as follows.

In the IST, the failure mode was similar to the thermal shock test. I believe the stress was being applied to the sample by the same mechanism as the thermal shock test.

By IST, the thermal cycling can be carried out in a shorter period of time than the thermal shock test.

### In addition, the issue involved with IST is as follows.

Although the faults become apparent with a smaller number of cycles in IST than in the thermal shock test, the acceleration of the test cycle is assumed to be different depending on the PCBs material (difference of CTE with copper).

It was found that the test results of IST are affected by plating thickness of samples. If evaluated under different conditions of plating thickness, through hole reliability due to other factors (drill condition and copper plating quality etc.) may not be correctly evaluated. Therefore, it is important to conduct failure analyses such as DPA.